

FCS_MainBoard

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Prepared by:

Shalaka Connected Devices LLP

info@shalaka.com

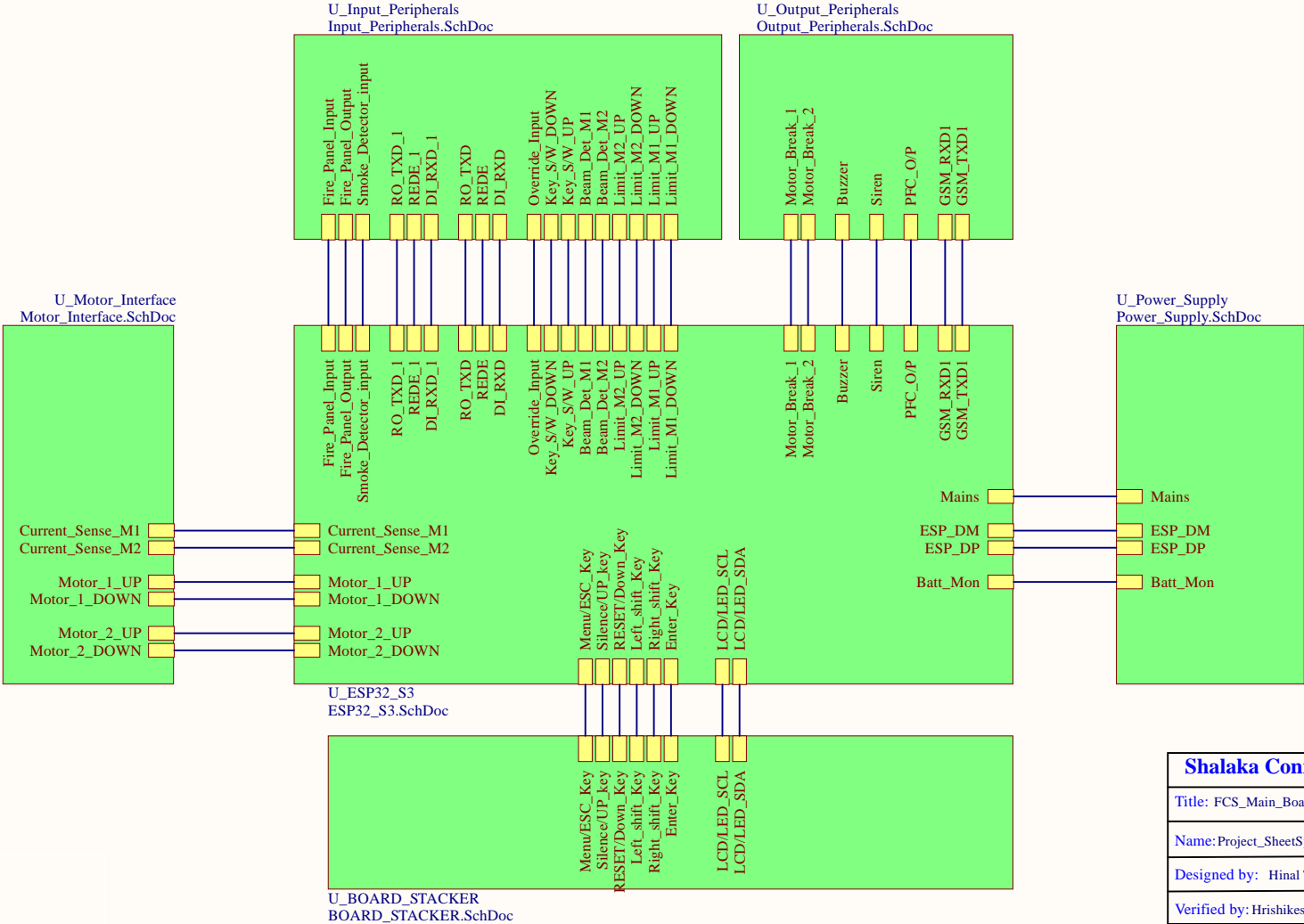
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| Shalaka Connected Devices LLP | |
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| Title: | FCS_Main_Board |
| Name: | Introduction.SchDoc |
| Designed by: | Hinal Tailor |
| Verified by: | Hrishikesh Kamat |
| Sheet: | 1 of 9 |
| Date: | 03-11-2023 |



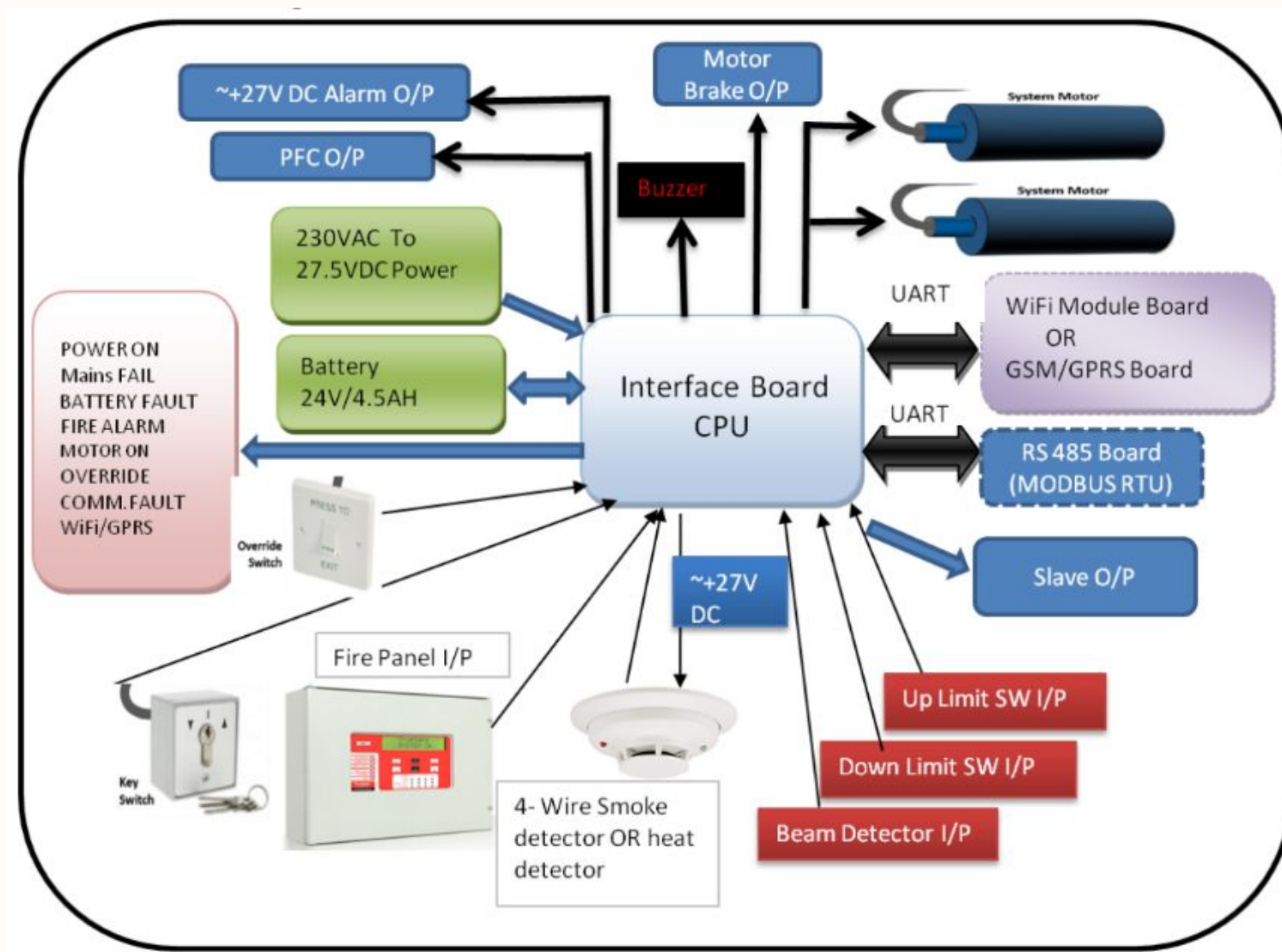
Shalaka

ProjectSheet_Symbol



| Shalaka Connected Devices LLP | |  Shalaka |
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| Name: | Project_SheetSymbol.SchDoc | |
| Designed by: | Hinal Tailor | |
| Verified by: | Hrishikesh Kamat | |
| Sheet: | 2 of 9 | Date: 03-11-2023 |

Block_Diagram



Shalaka Connected Devices LLP

Title: FCS_Main_Board

Name: Block_Diagram.SchDoc

Designed by: Hinal Tailor

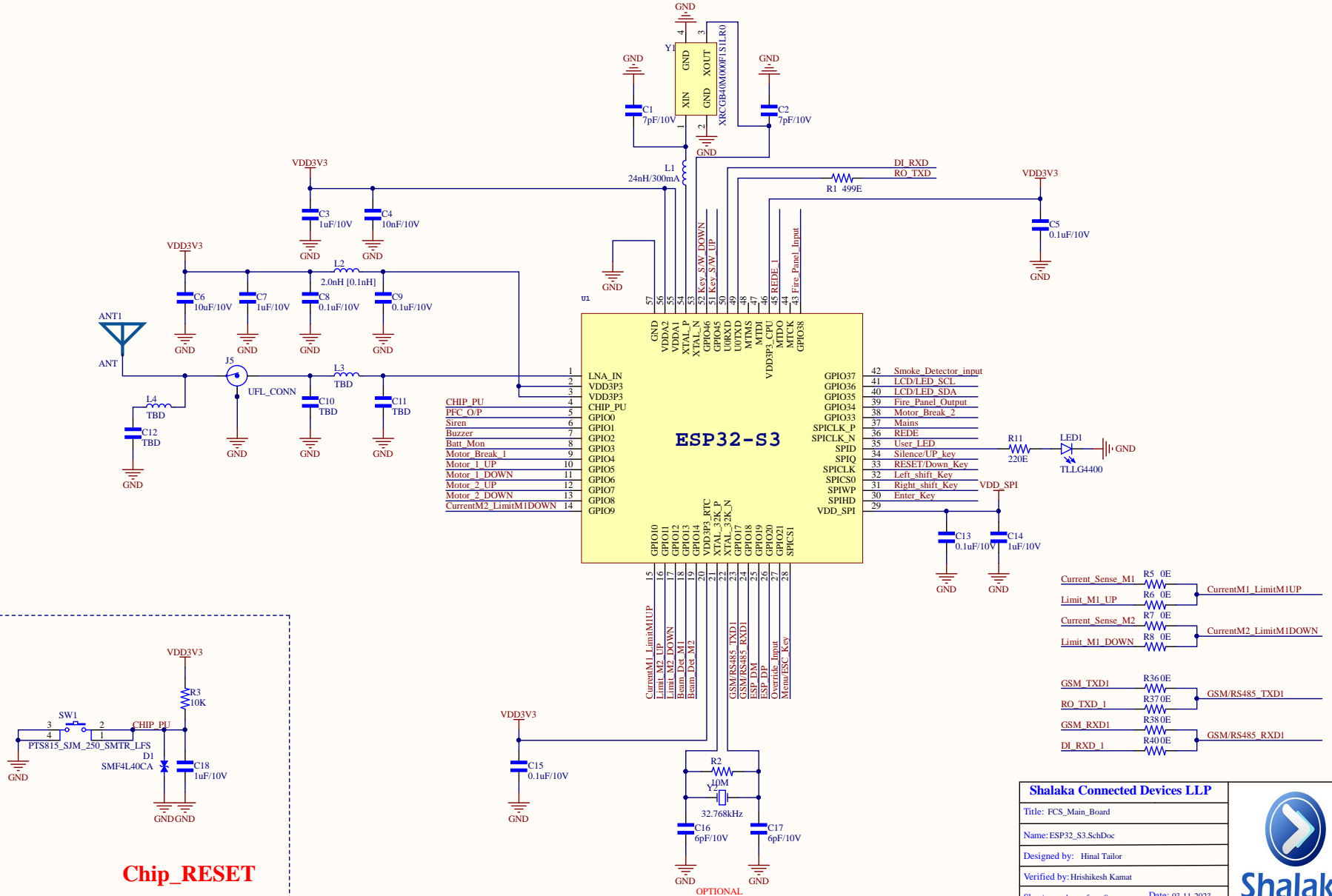
Verified by: Hrishikesh Kamat

Sheet: 3 of 9 Date: 03-11-2023

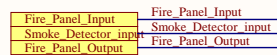


ESP32_S3_Interface

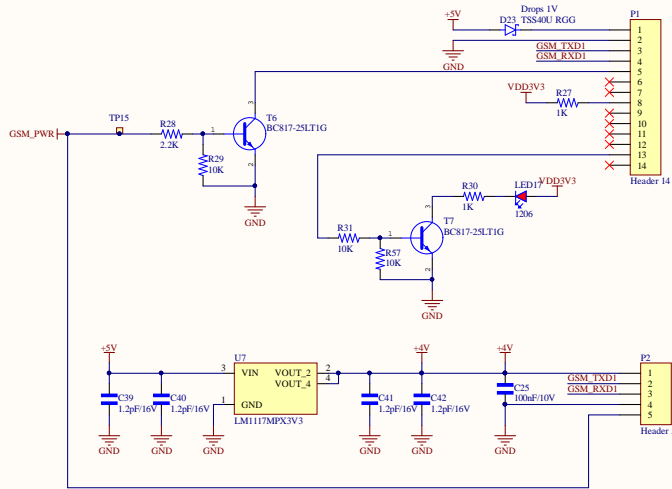
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| LCD/LED_SDA | LCD/LED_SDA |
| LCD/LED_SCL | LCD/LED_SCL |
| Beam_Det_M1 | Beam_Det_M1 |
| Beam_Det_M2 | Beam_Det_M2 |
| Limit_M2_UP | Limit_M2_UP |
| Limit_M2_DOWN | Limit_M2_DOWN |
| Limit_M1_UP | Limit_M1_UP |
| Limit_M1_DOWN | Limit_M1_DOWN |
| Key_S/W_DOWN | Key_S/W_DOWN |
| Key_S/W_UP | Key_S/W_UP |
| Current_Sense_M1 | Current_Sense_M1 |
| Current_Sense_M2 | Current_Sense_M2 |
| Mains | Mains |
| Batt_Mon | Batt_Mon |
| Override_Input | Override_Input |
| Fire_Panel_Input | Fire_Panel_Input |
| Smoke_Detector_input | Smoke_Detector_input |
| Fire_Panel_Output | Fire_Panel_Output |
| ESP_DM | ESP_DM |
| ESP_DP | ESP_DP |
| Motor_Break_1 | Motor_Break_1 |
| Motor_Break_2 | Motor_Break_2 |
| Menu/ESC_Key | Menu/ESC_Key |
| Silence/UP_Key | Silence/UP_Key |
| RESET/Down_Key | RESET/Down_Key |
| Left shift_Key | Left shift_Key |
| Right shift_Key | Right shift_Key |
| Enter_Key | Enter_Key |
| PFC_O/P | PFC_O/P |
| Siren | Siren |
| GSM_TXD1 | GSM_TXD1 |
| GSM_RXD1 | GSM_RXD1 |
| RO_TXD | RO_TXD |
| REDE | REDE |
| DI_RXD | DI_RXD |
| DI_RXD_1 | DI_RXD_1 |
| REDE_1 | REDE_1 |
| RO_TXD_1 | RO_TXD_1 |
| Buzzer | Buzzer |
| Motor_1_UP | Motor_1_UP |
| Motor_1_DOWN | Motor_1_DOWN |
| Motor_2_UP | Motor_2_UP |
| Motor_2_DOWN | Motor_2_DOWN |



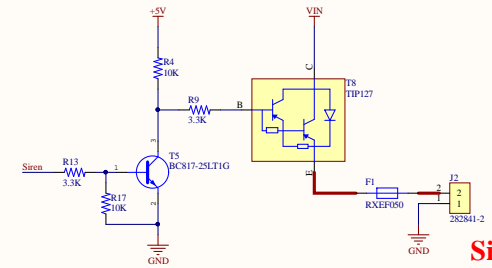
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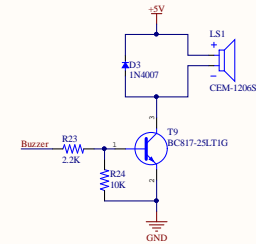
Output_Peripherals



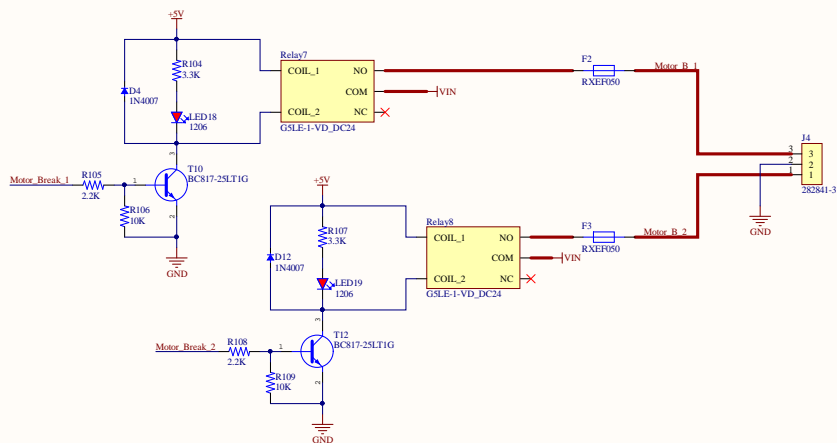
GSM_Interface



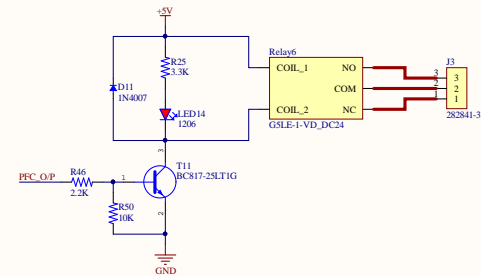
Siren_Interface



Buzzer_Interface



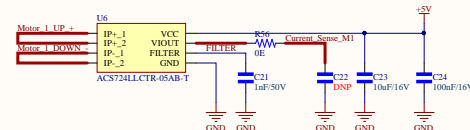
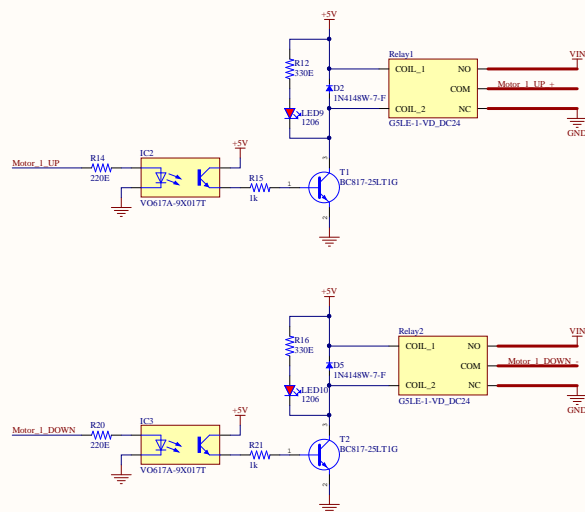
Motor_Break_Interface



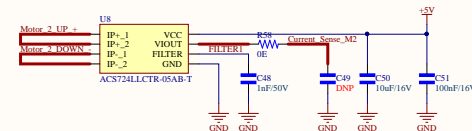
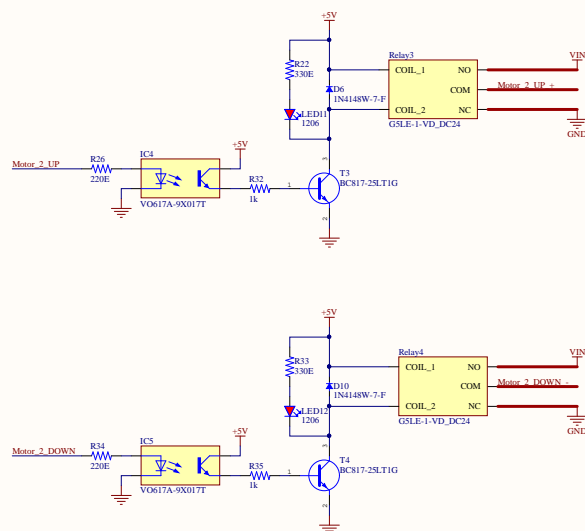
PFC_Interface

Motor_Interface

| | |
|------------------|------------------|
| Motor_1_UP | Motor_1_UP |
| Motor_1_DOWN | Motor_1_DOWN |
| Motor_2_UP | Motor_2_UP |
| Motor_2_DOWN | Motor_2_DOWN |
| Current_Sense_M1 | Current_Sense_M1 |
| Current_Sense_M2 | Current_Sense_M2 |

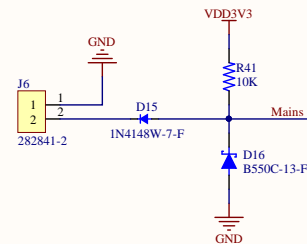
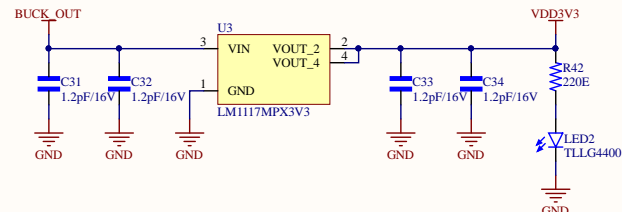
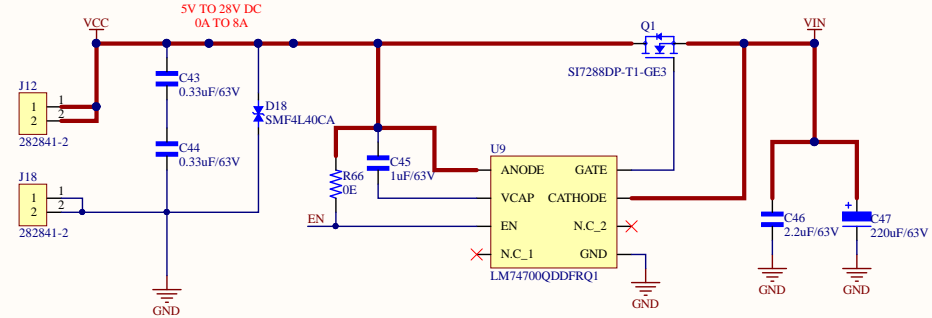
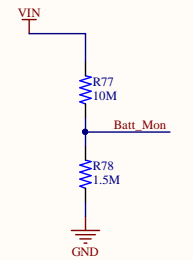
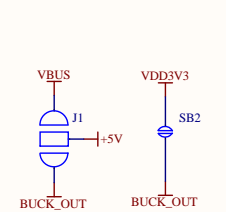
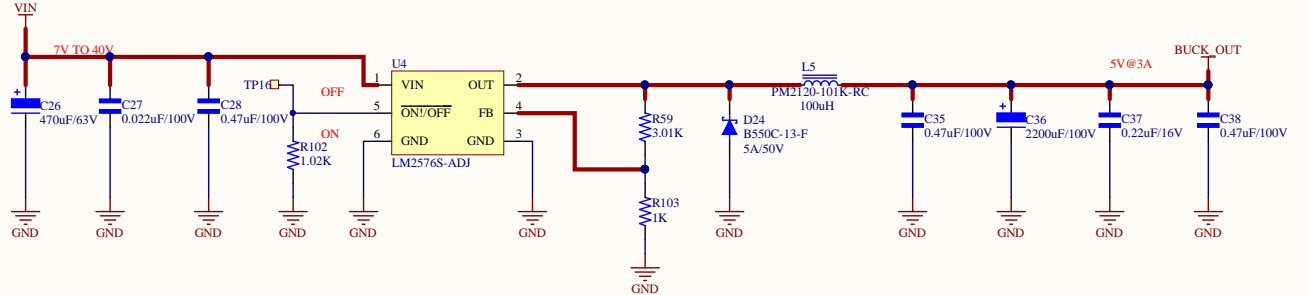
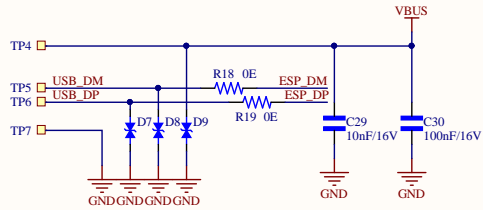
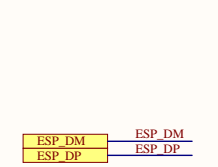


CHANNEL 1



CHANNEL 2

Power_Supply



Shalaka Connected Devices LLP

Title: FCS_Main_Board

Name: Power_Supply.SchDoc

Designed by: Hinal Tailor

Verified by: Hrishikesh Kamat

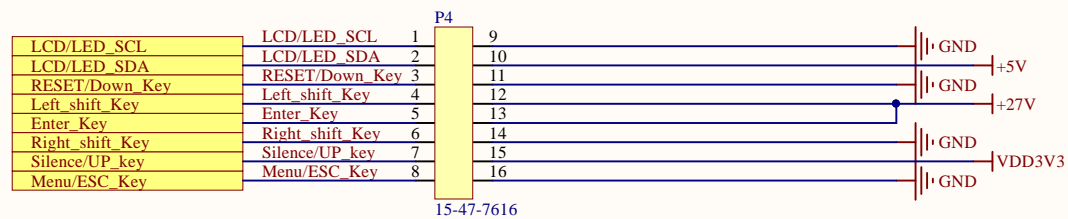
Sheet: 9 of 9

Date: 03-11-2023

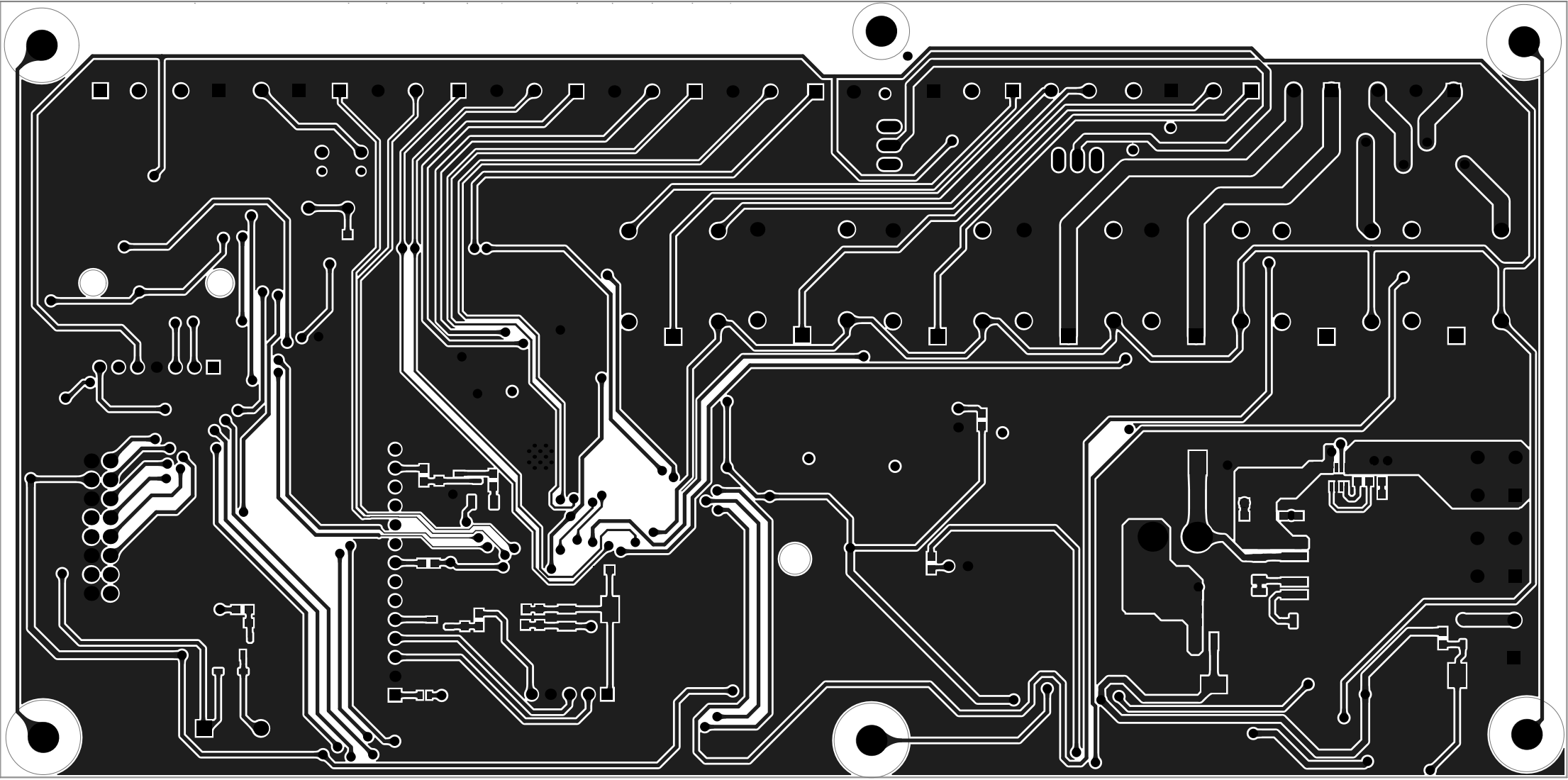


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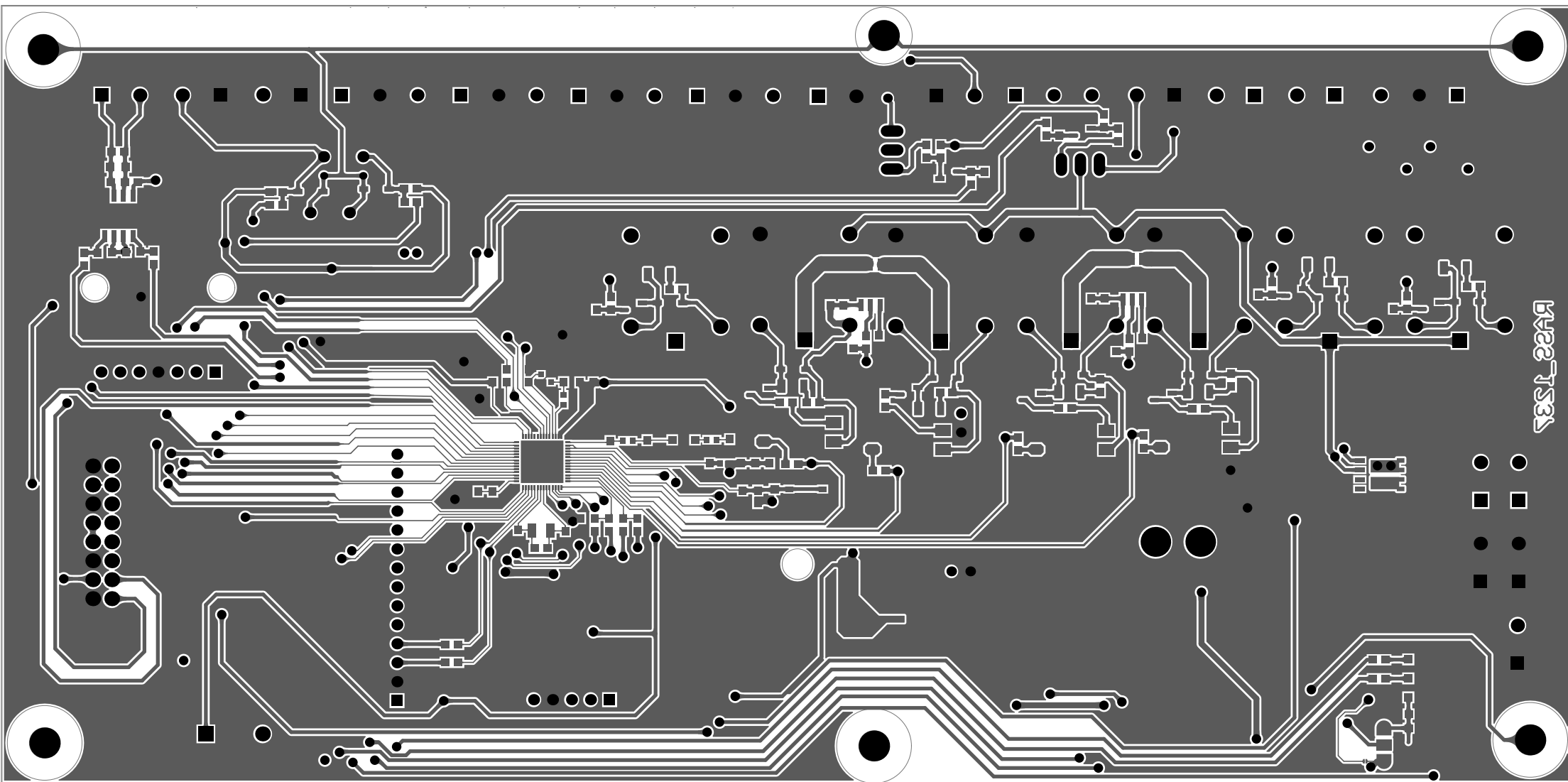
Board_Stacker



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|-------------------------------|----------------------|---|
| Shalaka Connected Devices LLP | |  Shalaka |
| Title: | FCS_Main_Board | |
| Name: | BOARD_STACKER.SchDoc | |
| Designed by: | Hinal Tailor | |
| Verified by: | Hrishikesh Kamat | |
| Sheet: | 8 of 9 | Date: 03-11-2023 |



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D7 D8 D3a C30
1b2 1b1 1b4
2B5 21

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D15
R102
R108
R104
D4
FED18
R106
R10e

F4
R85
R13
R12
R11

C19
C18
C17
C16

N8
C20
C21

R33
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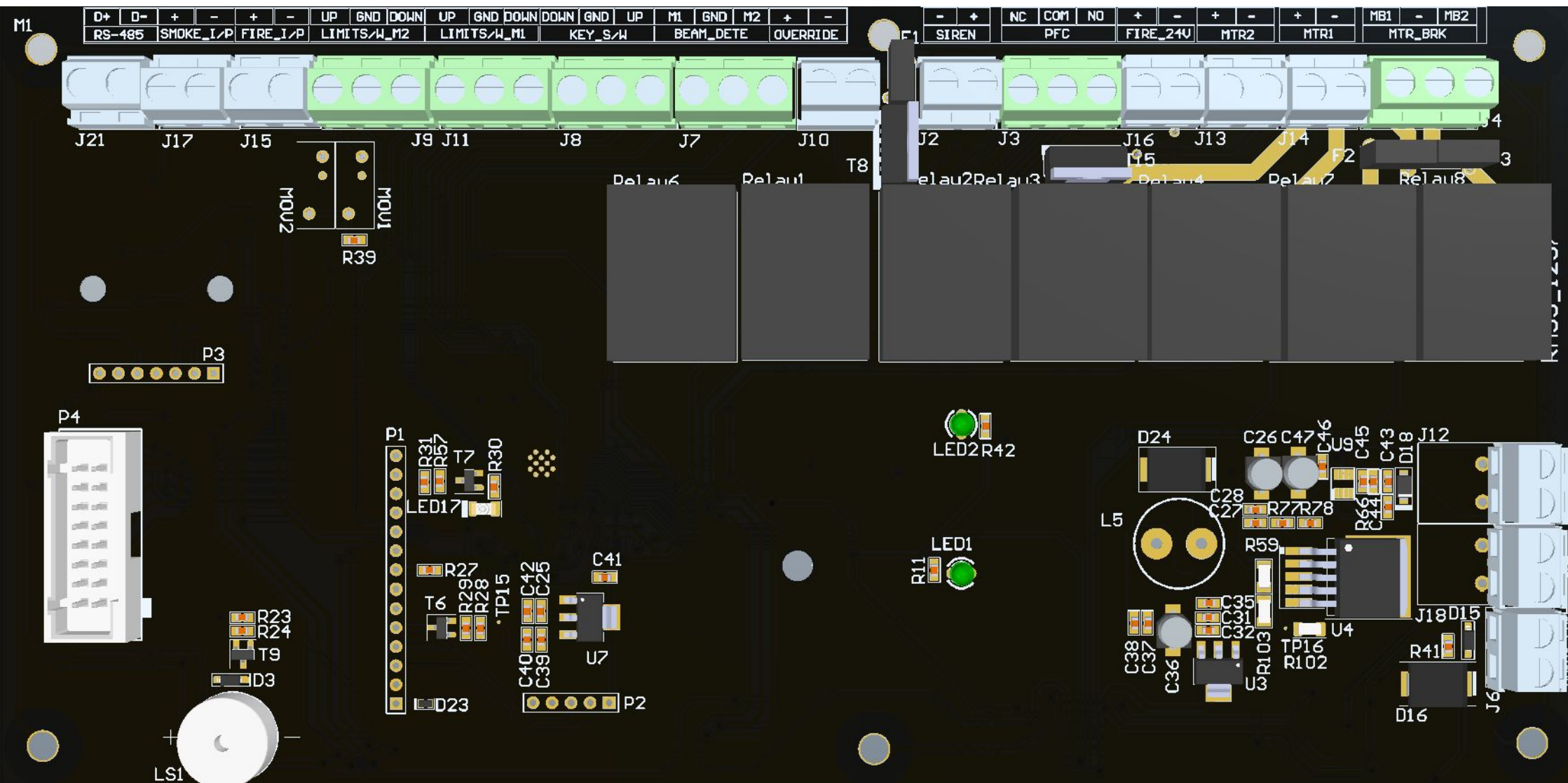
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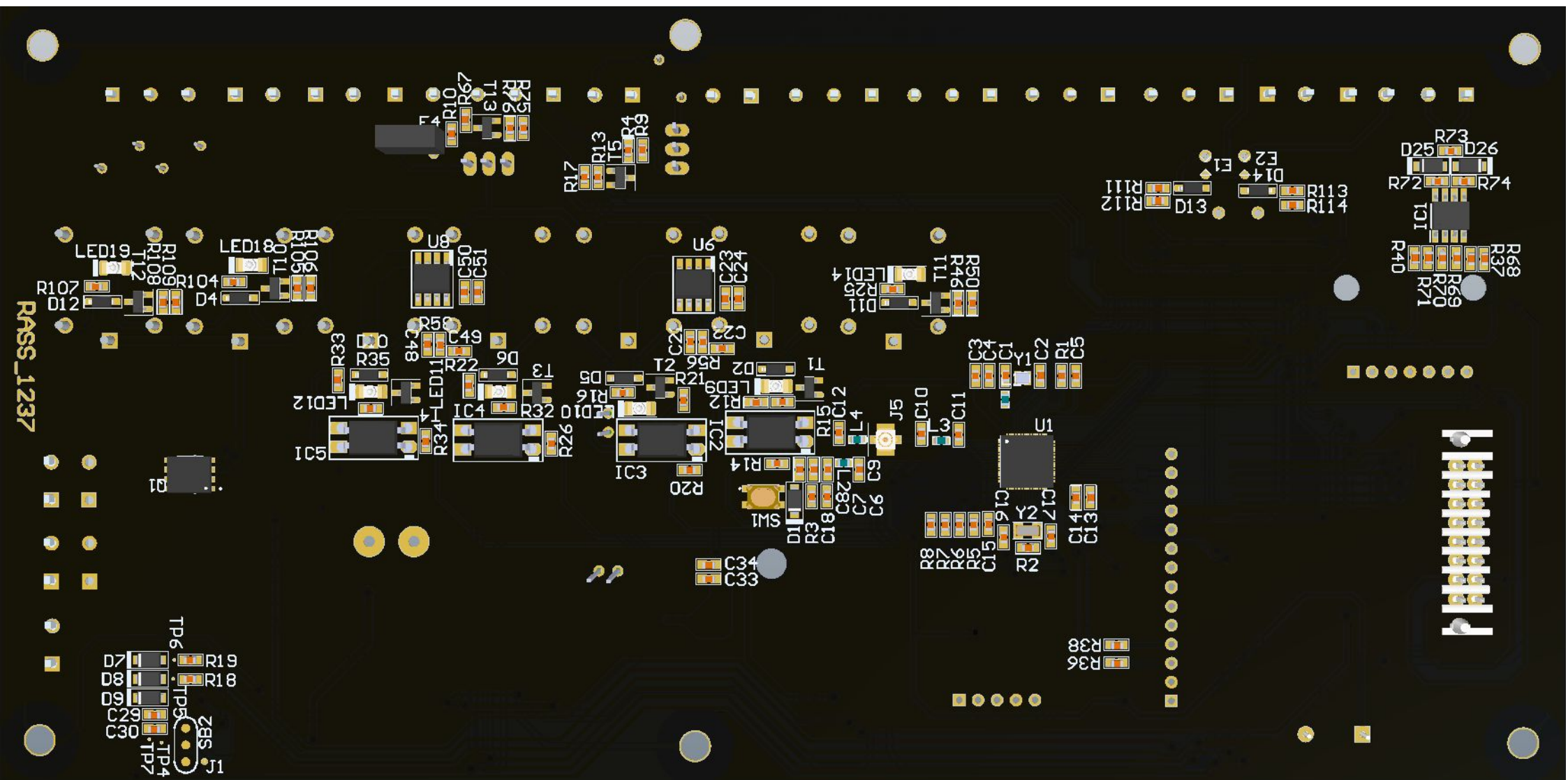
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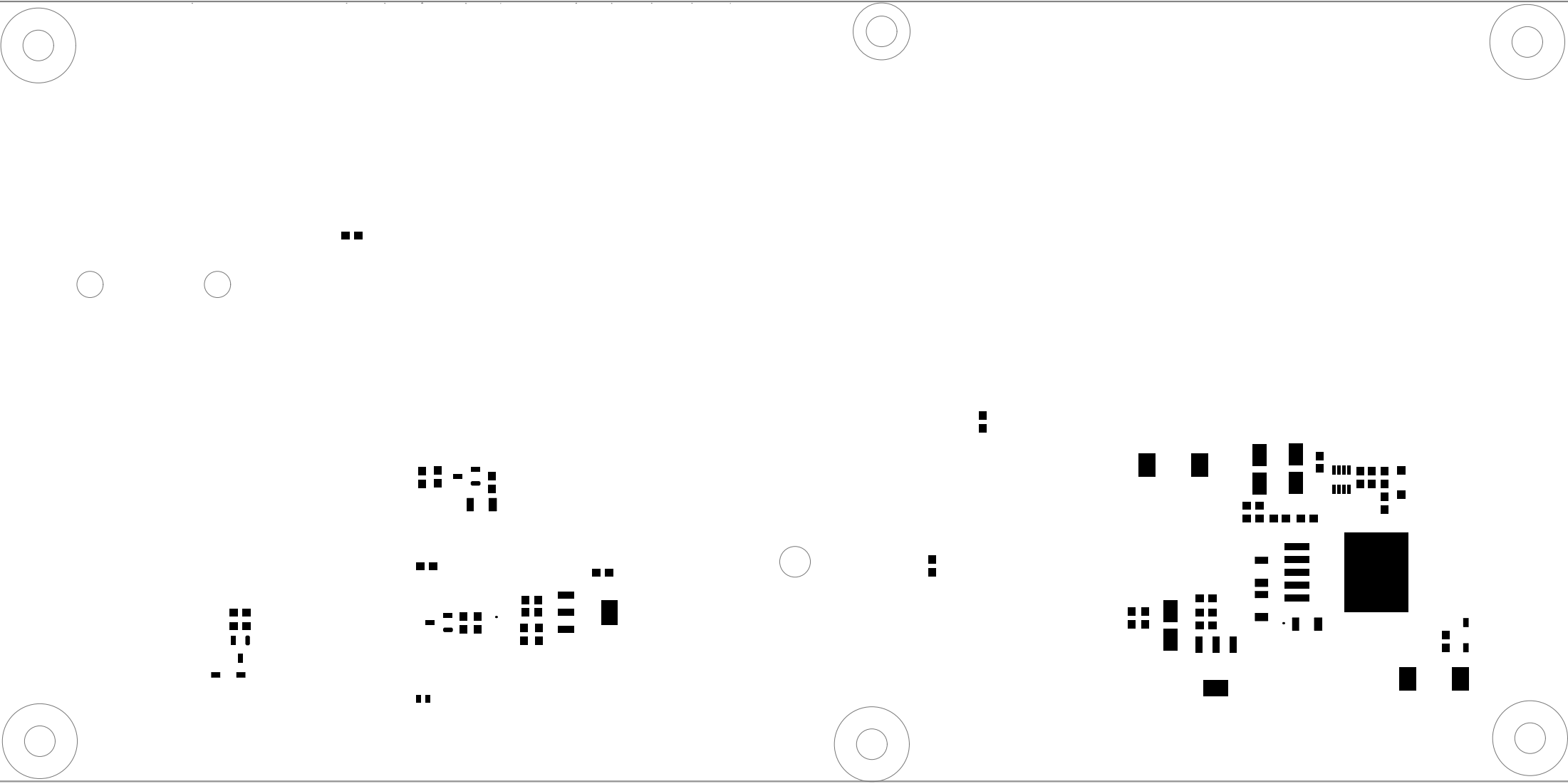
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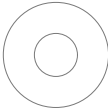
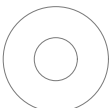
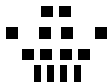
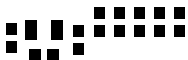
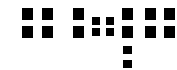
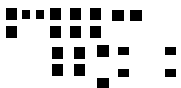
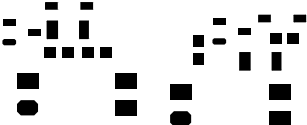
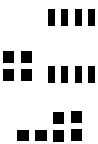
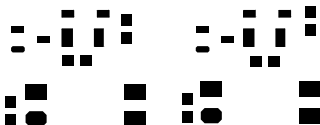
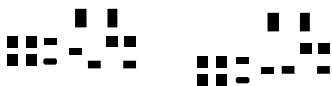
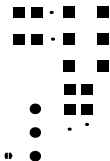
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RA22_1535



Bill of Materials for Project [FCS_Main_Board.PrjPcb] (No PCB Document Selected)

| | | |
|----------------|------------|-------------|
| Creation Date: | 03-11-2023 | 22:58 |
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